

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Biswajit Sur et al.

Title: ELECTRONIC ASSEMBLY COMPRISING SOLDERABLE THERMAL INTERFACE AND

METHODS OF MANUFACTURE

Docket No.: 884.319US2 Filed: February 9, 2004 Examiner: Andy Huynh

Customer No.: 21186

Commissioner for Patents

Attn: MAIL STOP ISSUE FEE

P.O. Box 1450

Alexandria, VA 22313-1450

Serial No.: 10/775,890

Due Date: April 17, 2006

Group Art Unit: 2818

Confirmation No.: 1421

Notice of Allowance Date:

January 17, 2006

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Customer Number: 21186

Ann M. McCrackin

Reg. No. 42,858 AMM1:CMG:clh

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Chris Hammond

Signature

PATENT S/N 10/775,890

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Applicant:

Biswaris Sur

Examiner: Andy Huynh

Serial No.: Filed:

10/775,890 February 9, 2004 Group Art Unit: 2818 Docket: 884.319US2

Title:

ELECTRONIC ASSEMBLY COMPRISING SOLDERABLE

THERMAL INTERFACE AND METHODS OF MANUFACTURE

Intel Corporation Assignee:

Customer No. 21186

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

Serial/Patent No. Filing Date/Issue Date

Attorney Docket

Title

09/652430 6724078

August 31, 2000 April 20, 2004

884.319US1

ELECTRONIC ASSEMBLY COMPRISING SOLDERABLE

THERMAL INTERFACE (As Amended)

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

> Respectfully submitted, BISWAJIT SUR ET AL. By their Representatives.

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